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Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on ER 408659660 US

11/19/03

(Date of Deposit)



Karen Cinq-Mars

(Signature)

11/19/03

(Date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of : November 19, 2003

Neves et al. :

Serial No. : Examiner:

Filed: Herewith : IBM Corporation
Dept. 18G/Bldg. 300-482
2070 Route 52
Hopewell Junction,
New York 12533-6531

Title: A METHOD TO IDENTIFY
GEOMETRICALLY NON-OVERLAPPING
OPTIMIZATION PARTITIONS FOR
PARALLEL TIMING CLOSURE :

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the document listed on the enclosed Form PTO-1449. A copy of the listed document is also enclosed.

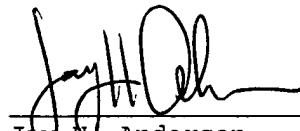
It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

FIS920030387US1

Applicants' undersigned attorney may be reached by telephone at (845) 894-3667.

All correspondence should continue to be directed to the below listed address.

Respectfully submitted,



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